

# MICROSAR Memory Course

Agenda VectorAcademy

<b>Delivery Format:</b>	This Course is offered in Classroom <b>or</b> Remote Format
<b>Duration:</b>	Classroom: 1 day Remote: 6 hours
<b>Target Group:</b>	ECU developers with focus on configuration and integration of a memory stack
<b>Prerequisites:</b>	Participation in the Training "AUTOSAR Classic Platform Basic Course" or a good knowledge about AUTOSAR Classic Platform
<b>Goal:</b>	Definition of sensible Memory partitions, correct handling of typical problems, dimensioning and optimization, usage of memory solutions to interact with the bootloader

## 1. Introduction | 0.5 h

- > Overview memory stack
- > Differences between EEPROM and flash

## 2. NvBlockSwComponents | 0.5 h

- > Placement within AUTOSAR
- > Introduction and features
- > NV data ports
- > Design of NvBlockSwComponents
- > NV block descriptor

## 3. Non-Volatile Memory Manager | 1.0 h

- > Placement within memory stack
- > Introduction and features
- > Configuration
- > FAQ

## 4. Memory Abstraction Interface | 0.5 h

- > Placement within memory stack
- > Basic functionality

## 5. MICROSAR EA - EEPROM Abstraction | 1.0 h

- > Placement within memory stack
- > Features
- > Principles of operation
- > Configuration
- > FAQ

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## 6. MICROSAR FEE - FLASH EEPROM Emulation | 1.0 h

- > Placement within memory stack
- > Features
- > Principles of operation
- > Configuration
- > FAQ

## 7. MICROSAR FEE SmallSector | 0.5 h

- > Placement within memory stack
- > Features
- > Principles of operation
- > Configuration
- > FAQ

## 8. vMem Solution | 0.5 h

- > Placement within memory stack
- > vMem solution architecture
- > AUTOSAR Flash Driver Interface: Fls\_30\_vMemAccM
- > Memory access management
- > Hardware access
- > Use cases

## 9. Analyzation of Memory Dumps | 0.5 h

- > Handling of MemAlyzer