

MICROSAR Memory Advanced Course

Agenda VectorAcademy

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| Delivery Format: | This Course is offered in Classroom or Remote Format |
| Duration: | Classroom: 1 day Remote: 7.5 hours |
| Target Group: | ECU developers with focus on configuration and integration of a memory stack |
| Prerequisites: | Participation in the Training "AUTOSAR Classic Platform Basic Course" or a good knowledge about AUTOSAR Classic Platform |
| Goal: | Definition of sensible Memory partitions, correct handling of typical problems, dimensioning and optimization, usage of memory solutions to interact with the bootloader |

1. Introduction

- > Overview Memory Stack
- > Differences between EEPROM and Flash

2. NvBlockSwComponents

- > Placement within AUTOSAR
- > Introduction and Features
- > NV Data Ports
- > Design of NvBlockSwComponents
- > NV Block Descriptor

3. Non-Volatile Memory Manager

- > Placement within Memory Stack
- > Introduction and Features
- > Configuration
- > FAQ

4. Memory Abstraction Interface

- > Placement within Memory Stack
- > Basic functionality

5. MICROSAR EA - EEPROM Abstraction

- > Placement within Memory Stack
- > Features
- > Principles of Operation
- > Configuration
- > FAQ

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6. MICROSAR FEE - FLASH EEPROM Emulation

- > Placement within Memory Stack
- > Features
- > Principles of Operation
- > Configuration
- > FAQ

7. MICROSAR FEE SmallSector

- > Placement within Memory Stack
- > Features
- > Principles of Operation
- > Configuration
- > FAQ

8. vMem Solution

- > Placement within Memory Stack
- > vMem Solution Architecture
- > AUTOSAR Flash Driver Interface: Fls_30_vMemAccM
- > Memory Access Management
- > Hardware Access
- > Use Cases

9. Analyzation of Memory Dumps

- > Handling of MemAlyzer