

MICROSAR Memory Advanced Course

Agenda VectorAcademy

Delivery Format:	This Course is offered in Classroom or Remote Format
Duration:	Classroom: 1 day Remote: 6 hours
Target Group:	ECU developers with focus on configuration and integration of a memory stack
Prerequisites:	Participation in the Training "AUTOSAR Classic Platform Basic Course" or a good knowledge about AUTOSAR Classic Platform
Goal:	Definition of sensible Memory partitions, correct handling of typical problems, dimensioning and optimization, usage of memory solutions to interact with the bootloader

1. Introduction

- > Overview memory stack
- > Differences between EEPROM and flash

2. NvBlockSwComponents

- > Placement within AUTOSAR
- > Introduction and features
- > NV data ports
- > Design of NvBlockSwComponents
- > NV block descriptor

3. Non-Volatile Memory Manager

- > Placement within memory stack
- > Introduction and features
- > Configuration
- > FAQ

4. Memory Abstraction Interface

- > Placement within memory stack
- > Basic functionality

5. MICROSAR EA - EEPROM Abstraction

- > Placement within memory stack
- > Features
- > Principles of operation
- > Configuration
- > FAQ

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6. MICROSAR FEE - FLASH EEPROM Emulation

- > Placement within memory stack
- > Features
- > Principles of operation
- > Configuration
- > FAQ

7. MICROSAR FEE SmallSector

- > Placement within memory stack
- > Features
- > Principles of operation
- > Configuration
- > FAQ

8. vMem Solution

- > Placement within memory stack
- > vMem solution architecture
- > AUTOSAR Flash Driver Interface: Fls_30_vMemAccM
- > Memory access management
- > Hardware access
- > Use cases

9. Analyzation of Memory Dumps

- > Handling of MemAlyzer