

# PREEvision Hardware Development

## Agenda VectorAcademy

<b>Delivery Format:</b>	This Course is offered in Classroom <b>or</b> Blended Format
<b>Duration:</b>	Classroom: 3 days Blended: 21 hours
<b>Target Group:</b>	System architects, development engineers, network designers, wiring harness designers
<b>Prerequisites:</b>	Experience in systems or component development
<b>Goal:</b>	Design, evaluate and optimize network, electric circuit and wiring harness in PREEvision

### 1. Concepts of PREEvision

- > PREEvision as a product for the whole architecture management
- > Development process
- > Layer model

### 2. Introduction to PREEvision

- > Starting PREEvision the first time
- > Projects
- > Windows and diagrams
- > Perspectives and views
- > Traceability and navigation

### 3. PREEvision in Multi User Mode

- > Lock / Commit
- > Reuse
- > Check in / Check out

### 4. The Layers Network Topology and Component Architecture

- > Creation and connection of components
- > Creation of bus systems
- > Connection of components to buses
- > Using types
- > Reusing components
- > Inner structure of components

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### 5. The Layer Electric Circuit

- > Synthesis functionality
- > Electric circuit artifacts

### 6. The Layer Wiring Harness

- > Wiring harness artifacts
- > Dependencies to upper layers
- > Using types

### 7. Wiring Harness Details

- > Connectors, Cables, Splices, Inline connectors

### 8. The Layer Geometrical Topology

- > Geometrical topology artifacts
- > Layer perspective
- > Mapping

### 9. The Wiring Harness Router

- > Configuration of the router
- > Constraints
- > Visualize the router's results

### 10. Questions, Feedback, Suggestions

- > Opportunity for Questions, Feedback and Suggestions